

Z10cc Odin

1U Ethernet Traffic Generator
chassis with 2 x 10Gbps ports
and 4 x 1Gbps SFP+ ports



Key Features

- Flexibility
- Price/ performance
- Ease of use
- Advanced architecture

The Z10cc Odin is a wire-speed 2-port 10Gbps and 4-port 1Gbps Ethernet traffic generator. Based on Xena's advanced architecture, the Z10cc Odin is a highly flexible and cost-effective solution for testing both 10G and 1G Ethernet at Layers 2-3. Available only in our 1U Compact chassis, this is a robust and easy-to-transport solution.

The Z10cc Odin comes complete with Xena's free XenaManager software - an easy-to-use GUI for handling both routine and advanced test schedules that includes XOA CLI, Xena2544, Xena1564, Xena3918 and Xena2889.

[Find out more here:](#)



PORT LEVEL FEATURES	
Interface category	10G and 1G Ethernet
Total number of test ports (software configurable)	2 x 10G and 4 x 1G (can also test 100Mbps & 10 Mbps)
Interface options	<ul style="list-style-type: none"> • 2 x 10GBASE-SR / LR / LRM / ER / Direct Attached Cable (DAC)* and • 4 x 10/100/1000BASE-T** 2) or 1000BASE-X (SFP-MSA) or 100BASE-FX*** or 100BASE-BX*** <p><i>*The interface implements discrete PHY devices with built in EDC support that employs sophisticated signal processing techniques to recover a 10 Gbps signal that has travelled over a dispersive Copper Direct attach cable and restore a bit- error rate of 10-12 or better.</i></p> <p><i>** Requires Finisar SFP transceivers FCLF-8521-P2BTL with sgmii host interface</i></p> <p><i>*** Requires Source Photonics SFP transceivers with sgmii host interface</i></p>
Number of physical interface form factor	2xSFP+ and 4xSFP
Port statistics	<ul style="list-style-type: none"> • Link state, FCS errors, pause frames, ARP/PING, error injections, training packet • All traffic: RX and TX Mbit/s, packets/s, packets, bytes • Traffic w/o test payload: RX and TX Mbit/s, packets/s, packets, bytes
Adjustable Inter FrameGap (IFG)	Configurable from 16 to 56 bytes, default is 20B (12B IFG + 8B preamble)
Transmit line rate adjustment	Ability to adjust the effective line rate by forcing idle gaps equivalent to -1000 ppm (increments of 10 ppm)
Transmit line clock adjustment	From -400 to 400 ppm in steps of 0.001 ppm (shared across all ports)
ARP/PING	Supported (configurable IP and MAC address per port)
Field upgradeable	System is fully field upgradeable to product releases (FPGA images and software)
Histogram statistics (counter size: 64 bits)	Two real-time histograms per port. Each histogram can measure one of RX/TX packet length, IFG, jitter, or latency distribution for all traffic, a specific stream, or a filter
Tx disable	Enable/disable of copper link
IGMPv2 multicast join/leave	IGMPv2 continuous multicast join, with configurable repeat interval
Oscillator characteristics	<ul style="list-style-type: none"> • Initial Accuracy is 3 ppm • Frequency drift over 1st year: ± 3 ppm (over 15 years: ± 15 ppm) • Temperature Stability: ± 20 ppm (Total Stability is ± 35 ppm)

TRANSMIT ENGINES	
Number of transmit streams per port	256 (wire-speed) Each stream can generate millions of traffic flows through the use of field modifiers
Test payload insertion per stream	Wire-speed packet generation with timestamps, sequence numbers, and data integrity signature optionally inserted into each packet.
Stream statistics (counter size: 64 bits)	TX Mbit/s, packets/s, packets, bytes, FCS error
Bandwidth profiles	Burst size and density can be specified. Uniform and bursty bandwidth profile streams can be interleaved
Field modifiers	16-bit header field modifiers with inc, dec, or random mode. Each modifier has configurable bit-mask, repetition, min, max, and step parameters. 2 modifiers per stream for 1G ports and 5 modifiers per stream for 10G ports.
Packet length controls	Fixed, random, butterfly, and incrementing packet length distributions. Packet length from 56 to 16384 bytes
Packet payloads (basic)	Repeated user specified 1 to 18B pattern, a 8-bit incrementing pattern
Error generation	Undersize length (56B min) and oversize length (16384 max.) packet lengths, injection of sequence, disorder, payload integrity, and FCS errors
TX packet header support and RX autodecodes	Ethernet, Ethernet II, VLAN, ARP, IPv4, IPv6, UDP, TCP, LLC, SNAP, GTP, ICMP, RTP, RTCP, STP, MPLS, PBB, or fully specified by user
Packet scheduling modes	<ul style="list-style-type: none"> • Normal (stream interleaved mode). Standard scheduling mode, precise rates, minor variation in packet inter-frame gap. • Strict Uniform. New scheduling mode, with 100% uniform packet inter-frame gap, minor deviation from configured rates. • Sequential packet scheduling (sequential stream scheduling). Streams are scheduled continuously in sequential order, with configurable number of packets per stream. • Burst. Packets in a stream are organized in bursts. Bursts from active streams form a burst group. The user specifies time from start of one burst group till start of next burst group.

RECEIVE ENGINE	
Number of traceable Rx streams per port	<ul style="list-style-type: none"> • 648 (wire-speed) for 1G ports • 2016 (wire-speed) for 10G ports
Automatic detection of testpayload for received packets	Real-time reporting of statistics and latency, loss, payload integrity, sequence error, and disorder error checking
Jitter measurement	Jitter (Packet Delay Variation) measurements on up to 32 streams, compliant to MEF10 standard with 8 ns accuracy
Stream statistics	<ul style="list-style-type: none"> • RX Mbit/s, packets/s, packets, bytes. • Loss, payload integrity errors, sequence errors, disorder errors • Min latency, max latency, average latency • Min jitter, max jitter, average jitter
Latency measurements accuracy	<ul style="list-style-type: none"> • $\pm 16/32$ ns (opto/elec) for 1G ports • ± 8 ns for 10G ports
Latency measurement resolution	8 ns
Number of filters:	<ul style="list-style-type: none"> • 6 x 64-bit user-definable match-term patterns with mask, and offset • 6xframe length comparat or terms (longer,shorter) • 6 x user-defined filters expressed from AND/OR'ing of the match and length terms.
Filter statistics	Per filter: RX Mbit/s, packets/s, packets, bytes.

CAPTURE	
Capture criteria	All traffic, stream, FCS errors, filter match, or traffic without test payloads
Capture start/stop triggers	Capture start and stop trigger: none, FCS error, filter match
Capture limit per packet	16 – 16384 bytes
Wire-speed capture buffer per port	16 kB for 1G ports and 64 kB for 10G ports
Low speed capture buffer per port (10Mbit/s speed)	4096 packets (any size)

HW SPECIFICATIONS	
Max. Power	<ul style="list-style-type: none"> AC Voltage: 100-240V Frequency: 50-60Hz Max. Power: 90W Max. Current: 0.8A with 120V supply, and 0.4A with 240V supply
Weight	<ul style="list-style-type: none"> 1.75" (4.45 cm) X 19" (48.26 cm) X 9.8" (25 cm) 10lbs (4.5 kg)
Environmental	<ul style="list-style-type: none"> Operating Temperature: 10 to 35°C Storage Temperature: -40 to 70°C Humidity: 8% to 90% non-condensing
Max Noise	Compact: 49 dBa
Regulatory	FCC (US), CE (Europe)

Ordering Information

Product Description

C-Odin-10G-4S-2P-Combi - XenaCompact 1U chassis with 10GE and 1GE test module with two SFP+ ports

Product Code

C-Odin-10G-4S-2P-Combi



Local sales offices are located throughout the world. Visit our website to find the most convenient location.

1-800-5-LeCroy • teledynelecroy.com

