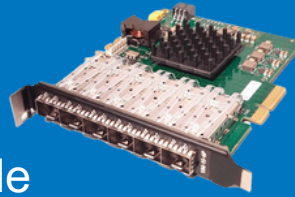


Z10s Odin

10GE 1-speed, 6-port test module
(SFP+ ports)



Key Features

- Price/performance
- Advanced architecture
- Ease of use

The Z10s Odin is a wire-speed 10Gbps Ethernet test module with 6 ports. Based on Xena's advanced architecture, the Z10s Odin is a proven solution for testing 10G Ethernet traffic at Layers 2-3. It is available for both the 4U 12-slot Xena B720/2400 chassis and the robust, transportable 1U XenaCompact chassis.

The Z10s Odin comes complete with our powerful, feature-rich XenaManager software - an easy-to-use GUI for handling both routine and advanced test schedules.

Also included is Xena OpenAutomation (XOA), an open-source test automation framework featuring a Python API that runs on any OS. Fast, easy to use and extremely flexible, XOA makes it easy to create tailored tests, as well as run standardized test methodologies like RFC2544, Y.1564, RFC3918 and RFC2889.

[Find out more here:](#)



PORT LEVEL FEATURES	
Interface category	10G Ethernet
Total number of test ports (software configurable)	6 x 10G
Interface options	6 x 10GBASE-SR / LR / ER / T 3) / Direct Attached Cable (DAC) 1)
Number of transceiver module cages	6xSFP+
Port statistics	<ul style="list-style-type: none"> • Link state, FCS errors, pause frames, ARP/PING, error injections, training packet • All traffic: RX and TX Mbit/s, packets/s, packets, bytes • Traffic w/o test payload: RX and TX Mbit/s, packets/s, packets, bytes
Adjustable Inter FrameGap (IFG)	Configurable from 16 to 56 bytes, default is 20B (12B IFG + 8B preamble)
Transmit line rate adjustment	Ability to adjust the effective line rate by forcing idle gaps equivalent to -1000 ppm (increments of 10 ppm)
Transmit line clock adjustment	From -400 to 400 ppm in steps of 0.001 ppm (shared across all ports)
ARP/PING	Supported (configurable IP and MAC address per port)
Field upgradeable	System is fully field upgradeable to product releases (FPGA images and Software)
Tx disable	Enable/disable of copper link
IGMPv2 multicast join/leave	IGMPv2 continuous multicast join, with configurable repeat interval
Histogram statistics 1)	Two real-time histograms per port. Each histogram can measure one of RX/TX packet length, IFG, or latency distribution for all traffic, a specific stream, or a filter
Oscillator characteristics	<ul style="list-style-type: none"> • Initial Accuracy is 3 ppm • Frequency drift over 1st year: +/- 3 ppm (over 15 years: +/- 15 ppm) • Temperature Stability: +/- 20 ppm (Total Stability is +/- 35 ppm)

TRANSMIT ENGINES	
Number of transmit streams per port	256 (wire-speed) Each stream can generate millions of traffic flows through the use of field modifiers
Test payload insertion per stream	Wire-speed packet generation with timestamps, sequence numbers, and data integrity signature optionally inserted into each packet.
Stream statistics 2)	TX Mbit/s, packets/s, packets, bytes, FCS error
Bandwidth profiles	Burst size and density can be specified. Uniform and bursty bandwidth profile streams can be interleaved
Field modifiers	16-bit header field modifiers with inc, dec, or random mode. Each modifier has configurable bit-mask, repetition, min, max, and step parameters. 5 modifiers per stream
Packet length controls	Fixed, random, butterfly, and incrementing packet length distributions. Packet length from 56 to 16384 bytes
Packet payloads (basic)	Repeated user specified 1 to 18B pattern, a 8-bit incrementing pattern
Error generation	Undersize length (56B min) and oversize length (16384 max.) packet lengths, injection of sequence, disorder, payload integrity, and FCS errors
TX packet header support and RX autodecodes	Ethernet, Ethernet II, VLAN, ARP, IPv4, IPv6, UDP, TCP, LLC, SNAP, GTP, ICMP, RTP, RTCP, STP, MPLS, PBB, or fully specified by user
Pause Frames	Responds to incoming pause and PFC (Priority-based Flow Control) frames
Packet scheduling modes	<ul style="list-style-type: none"> • Normal (stream interleaved mode): Standard scheduling mode, precise rates, minor variation in packet inter-frame gap. • Strict Uniform: New scheduling mode, with 100% uniform packet inter-frame gap, minor deviation from configured rates. • Sequential packet scheduling (sequential stream scheduling). Streams are scheduled continuously in sequential order, with configurable number of packets per stream. • Burst: Packets in a stream are organized in bursts. Bursts from active streams form a burst group. The user specifies time from start of one burst group till start of next burst group.

RECEIVE ENGINE	
Number of traceable Rx streams per port	2016 (wire-speed)
Automatic detection of testpayload for received packets	Real-time reporting of statistics and latency, loss, payload integrity, sequence error, and disorder error checking
Jitter measurement	Jitter (Packet Delay Variation) measurements compliant to MEF10 standard with 8 ns accuracy Jitter can be measured on up to 32 streams
Stream statistics	<ul style="list-style-type: none"> • RX Mbit/s, packets/s, packets, bytes. • Loss, payload integrity errors, sequence errors, disorder errors • Min latency, max latency, average latency • Min jitter, max jitter, average jitter
Latency measurements accuracy	±8 ns
Latency measurement resolution	8 ns (Latency measurements can calibrate and remove latency from transceiver modules)
Number of filters:	<ul style="list-style-type: none"> • 6 x 64-bit user-definable match-term patterns with mask, and offset • 6xframe length comparat or terms (longer,shorter) • 6 x user-defined filters expressed from AND/OR'ing of the match and length terms
Filter statistics	Per filter: RX Mbit/s, packets/s, packets, bytes.

CAPTURE	
Capture criteria	All traffic, stream, FCS errors, filter match, or traffic without test payloads
Capture start/stop triggers	Capture start and stop trigger: none, FCS error, filter match
Capture limit per packet	16 – 16384 bytes
Wire-speed capture buffer per port	64 kB
Low speed capture buffer per port (10Mbit/s speed)	4096 packets (any size)

HW SPECIFICATIONS	
Max. Power	10W
Weight	0.34 lbs (0.155 kg)
Environmental	<ul style="list-style-type: none"> • Operating Temperature: 10 to 35° C • Storage Temperature: -40 to 70° C • Humidity: 8% to 90% non-condensing
Regulatory	FCC (US), CE (Europe)

1. The interface implements discrete PHY devices with built in EDC support that employs sophisticated signal processing techniques to recover a 10 Gbps signal that has travelled over a dispersive Copper Direct attach cable and restore a bit-error rate of 10-12 or better.
2. Counter size: 64 bits
3. Requires SFP-10GTNC Prolabs 10G SFP+ 10GBASE-T

Ordering Information

Product Description

- Z10sc Odin XenaCompact 1U chassis with 10GE 1-speed, 6-port test module (SFP+ ports) for testing 10GBASE-SR/LR/DAC
- Z10s Odin 10GE 1-speed, 6-port test module (SFP+ ports) for testing 10GBASE-SR/LR/DAC

Product Code

C-Odin-10G-1S-6P
Odin-10G-1S-6P



Local sales offices are located throughout the world. Visit our website to find the most convenient location.

1-800-5-LeCroy • teledynelecroy.com

